Systematic LFT Derivation of Uncertain Electrical Circuits for the Worst-Case Tolerance Analysis

Moises Ferber, Anton Korniienko, Gérard Scorletti, Christian Vollaire, Florent Morel, and Laurent Krähenbühl

Abstract—In line with the trend toward continuous miniaturization and price reduction, it is crucial to analyze the impact of uncertainties on the performance of electrical circuits. Performance is evaluated for the worst-case scenario and in the frequency domain by computing upper and lower bounds. The purpose is not only to propose a method for the worst-case tolerance analysis but also to provide an efficient and a suitable tool for electrical engineers that can be easily applied to realistic electrical engineering problems. The proposed method is based on the robust analysis method (so-called μ -analysis) for which well known and efficient algorithms exist. However in order to apply it, the problem under consideration has to be transformed in a standard minimal so-called LFT representation. Its derivation is a difficult task even for control systems engineers. This paper proposes a transparent and systematic LFT derivation procedure for users based only on their knowledge of electrical engineering. At the end of this paper, an industrial example is provided, which reveals the benefits and the efficiency of the proposed approach, and how it can be applied to any linear electrical circuit.

Index Terms— μ -analysis, ν -analysis, linear fractional transformation (LFT), robustness analysis of circuits, uncertainty, worst-case analysis.

I. INTRODUCTION

N order to design a reliable system, it is required to take parametric variations into account and to ensure the *robust performance* of the system besides the *nominal* one. The robust performance *analysis* is the procedure allowing to test whether the system performance remains acceptable even in the presence of parameter variations or model *uncertainties*. Thus, the analysis of electrical and electronic systems taking into account their parametric uncertainty is an active research field in electromagnetic compatibility, as it can be shown by the growing number of publications [1]–[15].

There can be underlined two existing approaches to achieve the mentioned goal: 1) probabilistic and 2) worst-case analysis methodologies. The first approach allows us to determine the probability of the robust performance based on the given probability density functions over parameters [14], [16]–[19]. The worst-case approach in its turn does not consider any

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M. Ferber is with the Universidade Federal de Santa Catarina, Florianpolis 88040-900, Brazil (e-mail: moisesferber@gmail.com).

A. Korniienko, G. Scorletti, C. Vollaire, F. Morel, and L. Krähenbühl are with the University of Lyon, 69007 Lyon, France, and also with the Ecole Centrale de Lyon Ampere, 69134 Ecully, France (e-mail: Anton.Korniienko@ec-lyon.fr; Gerard.Scorletti@ec-lyon.fr; christian.vollaire@ec-lyon.fr; florent.morel@ ec-lyon.fr; Laurent.Krahenbuhl@ec-lyon.fr).

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probabilistic aspects, and the performance criteria is analyzed for the worst combination of the uncertain parameters [7]–[14].

In some critical applications, such as cardiac stimulation chips, nuclear power plant control, automatic driving panels of vehicles, etc., the ability to ensure the system performance in 100% of the cases is crucial. For these reasons, and due to the importance of such critical applications, the subject of this paper focuses on the worst-case tolerance analysis of electrical circuits. In this paper, only linear electrical circuit models are addressed. Even though the linearity assumption could appear restrictive, in a large number of applications, the linear model of an electrical circuit precisely describing the behavior of the system around an operation point can still be derived. Furthermore, the worst-case robust performance analysis even for the linear systems but with general (possibly large) size and structure is actually a challenging NP-hard problem [20]. This means that the execution time of the algorithm is a nonpolynomial function of the number of uncertainties.

There are a number of approaches to evaluate the worst-case system performance. In many applications related to the circuit theory, such as, for example, filter or phase-locked loop analysis, the performance is evaluated in the frequency domain. In this case, the robust system performance is assessed by computing upper and/or lower bounds on the frequency responses of some performance transfer functions. These transfer functions are chosen such that they reflect the performance measure of the circuit. For the filter example, the performance transfer function is the filter attenuation measure between filter inputs and outputs.

The main purpose of this paper is to propose a method for the worst-case tolerance analysis of any linear electrical circuit. The proposed method operates only in the frequency domain and should be efficient in terms of computational time (time grows as a polynomial function relative to the number of uncertainties) such that it is applicable for large-scale systems.

In [7], [8], and [10], a methodology based on interval arithmetic (IA) is proposed. In order to simplify the computations, the authors enforce the assumption of monotonicity of the variable of interest with respect to the uncertain parameters. Transposing to the problem under consideration, it means that the frequency response magnitude of a filter can only grow if any of the uncertain parameters grow. This is a very strong assumption restricting the number of the electrical circuits that can be analyzed. In [8], the authors propose an alternative solution based on interval partitioning, which is again inefficient in terms of the computation time for large-scale systems. Another approach improving the idea of interval partitioning based on constraint logic programming and relational interval arithmetic (RIA) is

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proposed in [9]. However, as claimed by the authors, the algorithm for the worst-case tolerance analysis proposed in [9] has exponential complexity with respect to the number of uncertain parameters and, therefore, is inefficient.

In [11], genetic algorithms and affine arithmetic (AA) are used to improve the results obtained from IA and RIA. However, the computational complexity is still high for systems of significant size and, therefore, its usage is somehow limited. An interesting solution to relax the monotonicity assumption and decrease the computation time is to encapsulate the bounds by outer and inner solutions as presented in [13]. Nevertheless the authors in [13] consider only the steady-states worst-case analysis and the dynamical aspects were put in perspective.

An interesting approach to address the problem considered in frequency domain is the μ -analysis methodology originally proposed in the control system theory community [21]–[23]. This methodology proposes to deal with the computation complexity by relaxing complex analysis conditions. It leads to the convex linear matrix inequality (LMI) optimization framework [24] for which efficient computation algorithms are available nowadays. These efficient algorithms allows us to take into account over 100 uncertain parameters without convergence problems [25]. The same idea of outer (inner) solutions, as in [13], is used to perform this relaxation so that the performance analysis result is still ensured in 100% of the cases. However, in contrast to [13], the μ -analysis can be applied for all frequencies, which allows us to include the dynamical system behavior and not only its steady states.

Nevertheless, though the μ -analysis method is usually accepted in the control theory community for the worst-case performance analysis, very few results were published on its application by the electronic engineering community. Few exceptions can be found in [26] and [27]. We believe that there are two main reasons that could explain this fact. The first reason is that the μ -analysis method is a powerful theoretically-based tool. It uses the control theory mathematical formalism, which is not necessary the same as in the electrical engineering community and is rarely adapted to its practice. As a consequence, the interesting ideas are not understood and are not transmitted for practical use in electrical circuit systems. The second reason is that in order to apply the μ -analysis, as it will be presented in this paper, a particular system transformation is necessary. This particular representation of the transformed system is called linear fractional transformation (LFT) or ΔM -representation. Once the LFT representation of the uncertain linear electrical circuit is derived, the application of the robust worst-case performance analysis is a quite routine method based on the resolution of convex optimization problems involving LMI constraints.

Even if it is theoretically possible to obtain the LFT for electrical circuits that model real applications, the transformation of an electrical schematic into a block diagram is a heavy timeconsuming task and, thus, it is not practically possible. It is then important to propose an efficient procedure to obtain an LFT representation directly from an electrical schematic. In the few existing results on the application of the μ -analysis to (uncertain) electrical circuit [26], [27], this crucial question was not addressed. The authors in [27] do not explain how to derive the LFT form, while in [26], the authors propose a manual and rather complex procedure based on matrix state-space representations and block scheme interconnection that strongly depends on the example under consideration.

The main contribution of this paper is to propose a systematic procedure in order to obtain, for any linear electrical circuit, an LFT representation of reasonable dimension using the formalism of electrical schematic. Based on this representation, blackbox procedures of the μ -analysis approach allows to investigate the worst-case performance analysis of arbitrary linear electrical circuits. The major benefit is that an electrical engineer can directly apply the μ -analysis to the worst-case tolerance analysis of arbitrary linear electrical circuits without special knowledge in control systems theory.

In Section II, the problem formulation is given. In Section III, based on the μ -analysis theoretical result, the worst-case upper bound problem is solved, and in Section IV, the systematic LFT representation derivation procedure of reasonable dimension is presented. In Section V, a general algorithm for the worst-case performance analysis is formulated. In Section VI, an application numerical industrial example is presented. This paper ends with a conclusion and further work discussions in Section VII.

Notations and Definitions are given as follows:

- 1) Conjugate transpose of a matrix F is denoted by F^* ;
- Singular values σ_i of a complex n × m matrix F are defined as square roots of eigen values λ_i of the matrix F*F if n ≥ m and FF* if n < m i.e. σ_i (F) = √λ_i (F*F) for n ≥ m or σ_i (F) = √λ_i (FF*) for n < m [28];
- σ_{max} (F) and σ_{min} (F) stand for the maximum and minimal singular value of the complex matrix F, respectively. For scalar matrix F, i.e., n = m = 1, σ_{max} (F) = σ_{min} (F) = |F|;
- 4) $\dim(w)$ denotes the dimension of the vector w;
- 5) I_n and $0_{n \times m}$, respectively, denote an $n \times n$ identity and $n \times m$ zero matrices. The dimensions could be omitted if apparent from the context.

II. PROBLEM STATEMENT

Electrical circuit models can be described by a schematic including components, such as resistors, inductors, and capacitors. Each of these elements is described by a parameter. For nominal parameters, electric circuit simulators can easily compute the nominal performance of the circuit model in the frequency domain. If the parameters are known as a nominal value and a given level of uncertainty, in order e.g., to ensure conformity to standards, the circuit designer has to evaluate the worst-case performance defined as, for a given frequency, the range of the magnitude of the frequency response for any possible values of the circuit parameters.

The μ -analysis can be applied to compute lower bound and upper bound on these ranges if the circuit with its uncertain parameters is expressed using an LFT representation, derivation of which is a difficult task. The problem under consideration is to extend the usual electrical schematic from the representation of "nominal" electrical circuits to "uncertain" ones such that an LFT representation of reasonable size is systematically obtained in order to evaluate the worst-case performance.

To solve this problem, let us proceed in two steps. First, in the next section, the problem of the worst-case tolerance analysis is introduced and its efficient solution is presented based on the existing results in control theory. This step allows us to illustrate how the LFT representation is used for the worstcase tolerance analysis, and to underline the importance of its systematic derivation. Second, in Section IV, a solution of the problem of the LFT representation derivation is proposed by introducing a library of "uncertain components" in addition to the usual library of "nominal components" of the electrical schematic.

III. WORST-CASE TOLERANCE ANALYSIS PROBLEM

A. Problem Formulation

Let us introduce the problem of electrical circuits worst-case tolerance analysis. For this purpose, the linear electrical circuit is modeled by a transfer function $T_{w_p \to z_p}(s)$ such that

$$z_p(s) = T_{w_p \to z_p}(s) w_p(s)$$

where $z_p(s)$ denotes the Laplace transform of the system output, $w_p(s)$ is the Laplace transform of the system input, and $T_{w_p \to z_p}(s)$ is such that with $p = (p_1, \ldots, p_N)$ the vector of the N parameters of the linear electrical circuit

$$T_{w_p \to z_p}(s) = \frac{\sum_{j=0}^{m} b_j(p) s^j}{\sum_{i=0}^{n} a_i(p) s^i}.$$
 (1)

The coefficients a_i and b_j of the transfer function $T_{w_p \to z_p}$ are in general rational functions of the N parameters of the linear electrical circuit

$$a_i(p) = a_i(p_1, ..., p_N)$$
 and $b_j(p) = b_j(p_1, ..., p_N)$

which are designed such that the transfer function frequency response $T_{w_p \to z_p}(j\omega)$ satisfies some performance specifications.

Unfortunately, due to the variation caused by phenomena, such as production dispersion and ageing, the actual value of the parameters p is different than the designed value p^0 . A more adequate modeling is then to assume that each parameter p_k belongs to an interval

$$p_k \in \left[p_k^{\min}, p_k^{\max}\right].$$
 (2)

The center of the interval $p_k^0 = \frac{p_k^{\max} + p_k^{\min}}{2}$ is referred to as the nominal value. If the actual value of p_k is not known, the lower bound p_k^{\min} and the upper bound p_k^{\max} are *a priori* known. These bounds must be finite and they are usually expressed as a relative expression of the nominal value p_k^0 , e.g.,

$$p_k = p_k^0 \pm 50\%.$$

The question is then to evaluate for any frequency ω the range of variation of $|T_{w_p \to z_p}(j\omega)|$ for any $p_k \in [p_k^{\min}, p_k^{\max}]$. This question can be decomposed into two different problems: the worst-case upper bound problem and the worst-case lower bound problem.

1) Worst-Case Upper Bound Problem: Given $T_{w_p \to z_p}$, p_k^{\min} and p_k^{\max} , for a given frequency ω compute the smallest $\eta(\omega)$ such that for any $p_k \in [p_k^{\min}, p_k^{\max}]$

$$|T_{w_p \to z_p}(j\omega)| \le \eta(\omega).$$

Another interesting problem is the computation of the worstcase lower bound on the frequency response of the transfer function $T_{w_p \to z_p}$: given $T_{w_p \to z_p}$, p_k^{\min} and p_k^{\max} , for a given frequency ω compute the largest $\beta(\omega)$ such that for any $p_k \in [p_k^{\min}, p_k^{\max}]$

$$\beta(\omega) \le |T_{w_p \to z_p}(j\omega)|.$$

Nevertheless in the sequel, we focus on the worst-case upper bound problem since the worst-case lower bound problem can be recast as an equivalent worst-case upper bound problem with the upper bound $\hat{\eta}(j\omega)$ defined as $\frac{1}{\beta(\omega)}$ and the transfer function \hat{T} defined as T^{-1}

 $\widehat{T}_{w_p \to z_p}$ defined as $T_{w_p \to z_p}^{-1}$. In the case when $z_p(s)$ and $w_p(s)$ are vectors (with $n_z = \dim(z_p)$ and $n_w = \dim(w_p)$) i.e. multi-input multi-output (MIMO) case, $T_{w_p \to z_p}(s)$ previously defined by (1) is now a matrix of transfer functions. The formulation of the worst-case upper bound problem is extended as follows.

2) MIMO Worst-Case Upper Bound Problem: Given $T_{w_p \to z_p}$, p_k^{\min} and p_k^{\max} , for a given frequency ω compute the smallest $\eta(\omega)$ such that for any $p_k \in [p_k^{\min}, p_k^{\max}]$

$$\sigma_{\max}\left(T_{w_p \to z_p}\left(j\omega\right)\right) \le \eta(\omega).$$

As in the single input, single output case, another interesting problem is the computation of the worst-case lower bound on the frequency response of the transfer function $T_{w_p \to z_p}$: given $T_{w_p \to z_p}$, p_k^{\min} and p_k^{\max} , for a given frequency ω compute the largest $\beta(\omega)$ such that for any $p_k \in [p_k^{\min}, p_k^{\max}]$

$$\beta(\omega) \le \sigma_{\min} \left(T_{w_p \to z_p} \left(j \omega \right) \right).$$

Nevertheless, in the sequel and for the same reasons as previously, we focus on the MIMO upper bound case only. In the next section, we reveal how the MIMO worst-case upper bound problem is solved using a robust control approach.

B. Robust Worst-Case Performance Analysis

According to (2), the uncertain parameters can be expressed as a sum of two parts: 1) the nominal part p_k^0 and 2) the uncertain part $p_k^0 p_k^g \delta_k$

$$p_k = p_k^0 (1 + \delta_k p_k^g) \tag{3}$$

where $p_k^0 = \frac{1}{2} \left(p_k^{\max} + p_k^{\min} \right)$ denotes the nominal or center parameter value, $p_k^g = \frac{p_k^{\max} - p_k^{\min}}{p_k^{\max} + p_k^{\min}}$ is the relative value of the uncertainty¹ (for 50% $\rightarrow p_k^g = 0.5$), and δ_k is the corresponding normalized uncertainty $|\delta_k| \leq 1$.

¹In (3), the relative uncertainty representation is introduced in order to limit the numerical problems during the further computations. However, the absolute uncertainty form can be used as well.



Fig. 1. LFT representation of the linear electrical circuit.

The transfer function $T_{w_p \rightarrow z_p}$ defined by (1) can then be expressed as

$$T_{w_p \to z_p}(s) = \frac{\sum_{j=0}^{m} \widehat{b}_j(\delta_1, \cdots, \delta_N) s^j}{\sum_{i=0}^{n} \widehat{a}_i(\delta_1, \cdots, \delta_N) s^i}.$$
 (4)

As the coefficients $\hat{a}_i(\delta_1, \ldots, \delta_N)$ and $\hat{b}_j(\delta_1, \ldots, \delta_N)$ are rational functions of $\delta_1, \ldots, \delta_N, z_p(s) = T_{w_p \to z_p}(s)w_p(s)$ can always be transformed as [29]:

$$\begin{bmatrix} z(s) \\ z_p(s) \end{bmatrix} = M(s) \begin{bmatrix} w(s) \\ w_p(s) \end{bmatrix} \text{ and } w(s) = \Delta(s) z(s) \quad (5)$$

where Δ is defined by

$$\Delta = \begin{bmatrix} \delta_1 I_{n_1} & 0 & 0 \\ 0 & \ddots & 0 \\ 0 & 0 & \delta_N I_{n_N} \end{bmatrix}$$

with n_i defining the number of repetitions of uncertain parameter δ_i in the uncertain circuit model.

This representation, denoted² as $\Delta \star M$, is referred to as a LFT representation of the linear electrical circuit. This representation can be interpreted as the feedback interconnection of the nominal part M (usually designed to be stable) and the unknown part Δ (see Fig. 1).

Let us introduce the uncertainty set $\underline{\Delta}$ defined as

$$\underline{\Delta} = \left\{ \Delta | \Delta = \begin{bmatrix} \delta_1 I_{n_1} & 0 & 0 \\ 0 & \ddots & 0 \\ 0 & 0 & \delta_N I_{n_N} \end{bmatrix}, \begin{array}{c} \forall k \\ |\delta_k| \le 1 \\ \delta_k \in \mathbb{R} \end{array} \right\}.$$
(6)

The MIMO worst-case upper bound problem can then be expressed as for a given ω : compute the smallest $\eta(\omega)$ such that for any $\Delta \in \underline{\Delta}$

$$\sigma_{\max}\left(\Delta \star M(j\omega)\right) \le \eta(\omega).$$

This problem referred to as the robust performance problem was largely investigated in the automatic control community. A solution is presented in the following theorem.

$$\sigma_{\max}\left(\Delta \star M(j\omega)\right) < \eta\left(\omega\right)$$

if and only if there exists an Hermitian matrix $\Phi(j\omega)$: $\Phi^*(j\omega) = \Phi(j\omega)$ such that³

(i)
$$\forall \Delta \in \underline{\Delta}, \begin{bmatrix} I \\ \Delta \end{bmatrix}^* \begin{bmatrix} \Phi_{11} & \Phi_{12} \\ \Phi_{12}^* & \Phi_{22} \end{bmatrix} \begin{bmatrix} I \\ \Delta \end{bmatrix} \ge 0 \quad (7)$$

and

$$(ii) \begin{bmatrix} M \\ I \end{bmatrix}^* \begin{bmatrix} \Phi_{11} & 0 & \Phi_{12} & 0 \\ 0 & I_{n_z} & 0 & 0 \\ \Phi_{21} & 0 & \Phi_{22} & 0 \\ 0 & 0 & 0 & -\eta^2 I_{n_w} \end{bmatrix} \begin{bmatrix} M \\ I \end{bmatrix} \le -\epsilon I \quad (8)$$

with $\epsilon > 0$.

Based on this theorem, the solution of the worst-case upper bound problem is readily obtained (see the following corollary).

Corollary 1 (SISO Case): For stable nominal system M(s)and for all possible $\Delta \in \underline{\Delta}$, for a given ω

$$\left|\Delta \star M(j\omega)\right| < \eta\left(\omega\right)$$

if and only if there exists an Hermitian matrix $\Phi(j\omega)$: $\Phi^*(j\omega) = \Phi(j\omega)$ such that (7) and (8) are satisfied with $n_z = n_w = 1$.

C. Computational Application of Theorem 1 and Corollary 1

For a given frequency ω , the application of Theorem 1 (respectively, Corollary 1) allows to exactly find the upper bound $\eta(\omega)$ on the maximum singular value (frequency response magnitude) of $T_{w_p \to z_p}$ for MIMO (respectively, SISO) case by computing an appropriate matrix $\Phi(j\omega)$ such that conditions (7) and (8) are satisfied.

However, it turns out that testing condition (7) is a difficult problem. Indeed, condition (7) has to be tested for all possible uncertainty Δ in the set $\underline{\Delta}$, i.e., an optimization problem involving an infinite number of constraints.

In order to obtain an efficient computation, the optimization problem is modified by:

- 1) parametrizing, i.e., defining a set $\underline{\Phi}$ of matrices $\Phi(j\omega)$ for which the condition (7) is always satisfied;
- testing the condition (8) for all Φ(jω) from this set, i.e., for all Φ(jω) ∈ Φ.

In the case of a set $\underline{\Phi}$, which is affine or defined by LMI constraints, the constraint (8) of Theorem 1 defines a convex optimization problem involving a finite number of LMI constraints which is efficiently solved in polynomial time. Nevertheless, this optimization problem computes an upper bound $\overline{\eta}(\omega)$ on $\eta(\omega)$ instead of computing $\eta(\omega)$ itself. The consequence is a possible conservatism of the obtained result, i.e., $\overline{\eta}(\omega)$ can be far away from $\eta(\omega)$. However, thanks to an appropriate choice of

Theorem 1 (MIMO case,[30], Th. 2.1): For stable nominal system M(s), for all possible $\Delta \in \underline{\Delta}$, for a given ω

²The symbol \star denotes the Redheffer star product (see e.g., [29]).

³The dependence on $j\omega$ (and on ω) is dropped for simplicity of notation.

the parametrization set $\underline{\Phi}$, the conservatism can be significantly reduced.

It is clear that the set $\underline{\Phi}$ depends on the uncertainty set $\underline{\Delta}$ and the better it describes the uncertainty throughout (7), the closer the relaxed upper bounds $\overline{\eta}(\omega)$ to the real ones $\eta(\omega)$ are. For our problem, a suitable set $\underline{\Phi}$ is defined⁴ by

$$\underline{\Phi} = \left\{ \Phi | \Phi = \begin{bmatrix} D_1 & 0 & G_1 & 0 \\ & \ddots & & \ddots & \\ 0 & D_N & 0 & G_N \\ -G_1 & 0 & D_1 & 0 \\ & \ddots & & \ddots & \\ 0 & -G_N & 0 & D_N \end{bmatrix} \right\}$$
(9)

with D_i and G_i are complex, full, $n_i \times n_i$ Hermitian, and, respectively, skew Hermitian frequency depending matrices, i.e., $D^* = D$ and $G^* = -G$. It is so-called D, G-scaling for the case of real uncertainties δ_i used in μ -analysis [21], [22]. For these reasons, the use of the Theorem 1 with structure of Φ defined by (9) can be interpreted in terms of standard μ -analysis.

Finally, the computation of the upper bounds (close to the real upper bounds) is, thus, achieved by solving for each frequency ω the following convex optimization problem under LMI constraints:

$$\min_{\eta^2, \Phi} \eta^2$$
subject to (8), for $\Phi \in \underline{\Phi}$ defined by (9). (10)

The program solving the optimization problem (10) in MAT-LAB environment, providing the robust toolbox is included in MATLAB distribution, is available online on the website [31].

The MIMO worst-case upper bound problem can be, therefore, efficiently solved in order to apply the worst-case performance analysis of the linear electrical circuit. However, this is possible only in the case when the LFT representation (5) corresponding to the electrical circuit is available. The next section presents how to obtain the LFT representation of reasonable size starting from the electrical circuit schematic.

IV. SYSTEMATIC LFT DERIVATION

In this section, a method of systematic LFT derivation for a general linear uncertain electrical circuits is presented. When a designer is constructing the model of an electrical circuit with an arbitrary software⁵, interconnecting the library components together, he is actually building at the same time the LFT representation of the circuit with a minimal number of components. The only question is how to extract this LFT representation [see $\Delta \star M$ in (5)] from the electric circuit model? For this purpose, once the electric circuit model is built, first the traditionally used component models (such as resistor, capacitor, inductor models, etc.) are replaced by uncertain component models proposed in

this section. The number and the ordering of these uncertain components automatically define the structure of the block Δ . Then, the extraction procedure is performed in order to obtain the matrix transfer function M(s).

In the following section, a library of elementary uncertain linear electric circuit components is proposed. Since these uncertain components are very similar to the standard ones, there is only the need of regular electrical engineer's knowledge to use (and build) the uncertain model. It is even possible to combine the regular and uncertain components in order to avoid timeconsuming computation in the case where some uncertainties can be neglected. Then, in next section, the procedure of LFT representation extraction is presented.

A. Block Diagram of Uncertain Electrical Components

The detailed description of the uncertain components is given only for resistor, capacitor, and mutual inductor, while other components can be deduced in a similar fashion. A more complete table (or library) of uncertain components is then presented.

1) Resistor: The equivalent circuit of the normalized uncertain resistor is presented in the first row of Table I, where R_g is the unit less normalization factor, R_0 is the resistor nominal value (Ω), δ_R is a real number such that $|\delta_R| < 1$, z_R is the uncertain output, w_R is the uncertain input, i_R is the current that flows through the resistor, and v_R is the voltage drop across the resistor.

Note that the voltage seen by the terminals + and – of the circuit is the voltage drop on the resistor R_0 plus the voltage of the source in series with the resistor, which is given by $R_0 i_R R_g \delta_R$. The total voltage drop between terminals + and – of the circuit is given by (11).

Hence, the equivalent resistance of the circuit corresponds exactly to the normalized relative form of uncertain parameter introduced in (3).

2) *Capacitor:* The equivalent circuit of the normalized uncertain capacitor is presented in the second row of Table I.

Note that the current that flows through the terminals + and – of the circuit is the current that flows through the capacitor C_0 plus the current that is injected by the source in parallel with the capacitor C_0 , which is given by $C_0 \frac{dv_C}{dt} C_g \delta_C$. The total current of the circuit is given by (12).

Hence, the equivalent capacitance of the circuit exactly corresponds to the normalized relative form of uncertain parameter introduced in (3).

3) Mutual Inductor: The equivalent circuit of the normalized uncertain mutual inductor is presented in the last row of Table I.

Note that the voltage v_1 seen by the terminals + and – in the left part of the circuit is the voltage drop on the inductor L_1 , which is given by $L_1 \frac{di_{L_1}}{dt}$, plus the voltage drop on the mutual inductor M_0 , which is given by $-M_0 \frac{di_2}{dt}$ and plus the voltage of the source in series with M_0 , which is given by $-M_0 \frac{di_2}{dt} M_g \delta_M$. Thus, the total voltage v_1 in the left part of the circuit is given by (15).

⁴The dependence on $j\omega$ (and on ω) is dropped for simplicity.

⁵The example presented is SimPowerSystems toolbox.

TABLE I UNCERTAIN COMPONENT LIBRARY



$$v_R^{tot} = R_0 i_R + R_0 i_R R_g \delta_R = R_0 (1 + R_g \delta_R) i_R \tag{11}$$



$$i_C^{tot} = C_0 \frac{dv_C}{dt} + C_0 \frac{dv_C}{dt} C_g \delta_C = C_0 (1 + C_g \delta_C) \frac{dv_C}{dt}$$
(12)



$$v_L^{tot} = L_0 \frac{di_L}{dt} + L_0 \frac{di_L}{dt} L_g \delta_L = L_0 (1 + L_g \delta_L) \frac{di_L}{dt}$$
(13)



 $i_G^{tot} = G_0 v_G + G_0 v_G G_g \delta_G = G_0 (1 + G_g \delta_G) v_G$ (14)





Fig. 2. Systematic extraction of the LFT representation.

The procedure for obtaining v_2 is analogous. Other uncertain components from the proposed library are obtained in similar fashion and are presented in the Table I.

B. LFT Extraction

In the last section, equivalent uncertain linear electrical circuits for uncertain linear components were developed. All these equivalent circuits have electrical terminals, for the usual electrical connections, but also two or four uncertain terminals, an input w_x and output z_x , which should be connected to the uncertain block Δ .

Thus, to automatically derive the matrix of transfer functions M of any uncertain electrical circuit, one must follow the steps below:

- connect the equivalent uncertain elements by their electrical terminals creating the desired circuit topology;
- 2) connect inputs and outputs to the uncertain terminals respecting the ordering (the *i*th element should have input w_i and output z_i) according to the Δ structure (see Fig. 2);
- connect the desired performance analysis input (voltage or current source) and output (voltage or current measurement) (see Fig. 2);
- 4) compute a linear state-space model of M and then the matrix transfer function M(s). This last step can be performed, for example, in MATLAB Simulink using the functions linmod (...) and ss (...).

In this section, the systematic LFT derivation procedure was presented. Performing it together with the optimization problem (10) from Section III allows us to solve the MIMO worst-case upper bound problem. The overall algorithm solving the worstcase tolerance analysis problem of linear electrical circuit, i.e., upper and lower frequency bound computation, is given in the next section.

V. GENERAL ALGORITHM FOR THE WORST-CASE PERFORMANCE ANALYSIS

As explained in the Section III, the lower bound is obtained similarly to the upper bound by inverting the performance transfer function $T_{w_p \to z_p} = \Delta \star M$ and computing the upper bound of the inverted system $(T_{w_p \to z_p})^{-1} = (\Delta \star M)^{-1}$

$$\left|T_{w_{p}\to z_{p}}\left(j\omega\right)\right|^{-1}\leq\beta^{-1}\left(\omega\right)\Leftrightarrow\beta\left(\omega\right)\leq\left|T_{w_{p}\to z_{p}}\left(j\omega\right)\right|.$$



Fig. 3. Electrical schematic of an EMC Filter.

To do so, there is no need to recompute a new LFT representation and build a new electrical circuit. The corresponding inverse matrix transfer function M_{inv} can be computed based on the already computed direct matrix transfer function M and thanks to the following LFT inversion relation [32]:

$$M_{\rm inv} = \begin{bmatrix} M_{11} - M_{12}M_{22}^{-1}M_{21} & M_{12}M_{22}^{-1} \\ -M_{22}^{-1}M_{21} & M_{22}^{-1} \end{bmatrix}.$$
 (11)

The overall algorithm for the worst-case tolerance analysis of linear electrical circuits is now given.

Algorithm 1: Algorithm of the worst-case performance analysis of the linear electrical circuits

- 1) Build the regular nominal electrical circuit model;
- Derive the LFT representation of this electrical circuit, i.e., Δ * M according to Section IV;
- 3) Determine the matrix of transfer functions M_{inv} corresponding to the inverse LFT, applying (16);
- Define the frequency grid vector and, for each frequency ω, compute the frequency responses M (jω) and M_{inv} (jω);
- 5) Compute the upper bounds (on the exact upper bounds) $\eta(\omega)$ and $\hat{\eta}(\omega)$ solving the optimization problem (10) for $M(j\omega)$ and $M_{inv}(j\omega)$, respectively, and for each frequency value;
- The upper and lower bounds on the frequency response magnitude are η (ω) and β (ω) = η⁻¹ (ω).

VI. APPLICATION

The methodology presented in the previous section will be applied to an EMC filter. Its electrical schematic is shown in Fig. 3. It is a linear electrical circuit model with standard components R, C, L, etc. Since the filter is fabricated in industrial scale, technological dispersions causing parameter variations of these components are inevitable. In this example, there are 12 resistors, nine inductors, and five capacitors (including parasitic inductances and capacitances), which corresponds to 26 uncertain parameters. Computing the upper and lower bounds of the filter frequency response magnitude is, therefore, crucial in order to ensure an acceptable level of filtering performance.

Each electrical component is replaced by its uncertain block diagram and the matrices M and M_{inv} of the filter are readily obtained from the diagram presented in Fig. 4 using linmod function of MATLAB.

As presented in Fig. 4, the structure of the model does not change with respect to the nominal model in Fig. 3, i.e., the same number of hierarchical levels, the same interconnection subsystems, etc. The only change concerns the additional uncertain inputs and outputs appearing for each uncertain component model. These inputs/outputs are propagated through every hierarchical level. Only last, global hierarchical level is shown in Fig. 4.

A frequency grid covering typical conducted EMI standard's for this case was chosen. The computation of upper and lower bounds was then performed thanks to the function available [31]. The final results are presented in Fig. 5. In this figure, the full blue line represents the nominal filter frequency response magnitude. The red lines correspond to the computed upper and lower bounds, while dashed-dot green lines depict the frequency response magnitudes of the filter for the random chosen parameter combinations. For comparison reasons, the classical Monte–Carlo simulations were performed in order to compute the upper and lower frequency bounds as well.

As it can be seen in Fig. 5, no frequency response magnitude of randomly chosen parameter combinations (dashed-dot green lines) exceeds the bounds computed by our approach (red lines). Admittedly in contrast to our worst-case analysis approach, where the bounds are ensured, the Monte–Carlo bounds are only underestimation. Despite this, since Monte–Carlo simulations are a reference for robust performance analysis of electrical circuits (as well as for other domains), they should be discussed and compared with our approach. Furthermore, both the Monte–Carlo approach and our approach can be applied for any models of electrical circuit of arbitrary size and hierarchical structure.

First, the corresponding upper and lower bounds are close for both approaches. It is true that the Monte-Carlo bounds are tighter than those of the μ -analysis. There are two reasons which explain the existence of such a gap. The first is, as discussed previously, the possible conservatism of the result. This aspect can be improved by considering new types of parametrization sets Φ for the optimization problem (10) (see for example [30]). The second reason is, of course, the fact that the Monte–Carlo bounds are an estimation. This means that there are possible combinations of parameter values that may exceed the computed Monte-Carlo bounds. Ideally, if we could perform an infinite number of Monte-Carlo simulations, the new bounds will be closer to those of the μ -analysis. This is confirmed by comparing Monte–Carlo bounds between 10^4 and 10^5 samples. The case of 10^5 samples ensures a higher probability that the frequency response magnitude is inside of the computed bounds and it can be seen that these bounds approach the μ -analysis upper and lower bounds.

Another comparison aspect is the computational efforts for two approaches. To obtain the bounds for 10^4 samples, the



Fig. 4. Systematic LFT derivation of an EMC Filter.



Fig. 5. Bounds computation results for an EMC Filter.

computation time of Monte–Carlo approach is $t_{\rm MC}^4 = 3$ h and for the case of 10^5 samples $t_{\rm MC}^5 = 28$ h, whereas, in order to compute the bounds by the μ -analysis approach, ensured for all 100% cases, the computation time obtained is only $t_{\mu} = 17$ min. The proposed method is almost 100 times quicker. These time comparisons were carried out on a Intel i7-2860QM Quad-Core 2.5 GHz. Thus, there is ample evidence of advantages of the proposed method when compared to the Monte–Carlo simulations.

VII. CONCLUSION

In this paper, a frequency domain worst-case tolerance analysis method for an uncertain linear electrical circuits was presented. The method is based on a robust control theory approach, the so-called μ -analysis. To apply the μ -analysis, a particular LFT transformation problem must be considered first. The algorithm for systematic LFT derivation and worst-case tolerance analysis of linear electrical circuits was presented. To this purpose, a library of uncertain electrical components was created. The numerical example of EMC filter proves the efficiency of the proposed approach compared to the classical Monte–Carlo methodology.

Future works related to this paper will be an extension of the proposed approach to the case of the worst-case tolerance analysis along ranges of frequency, so that the appropriate choice of frequency grid can be easily performed. Other interesting perspectives are the systematic LFT reduction and the extension to the case of nonlinear electrical circuits including switches.

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Moises Ferber was born in Belo Horizonte, Brazil, in 1985. He received the B.Eng. and Ph.D. degrees in electrical engineering from the Federal University of Minas Gerais, Belo Horizonte, Brazil, in 2010 and 2013, respectively. He completed the Ph.D. degree in a joint supervision with Laboratoire Ampère, Ecole Centrale de Lyon, Ecully, France.

He is currently an Assistant Professor in electrical engineering at the Federal University of Santa Catarina, Joinville, Brazil. His research interests include electromagnetic compatibility, optimization,

and uncertainty quantification.



Anton Korniienko received the Ph.D. degree in automatic control, from the University of Lyon, Lyon, France. He received the degree from the National Technical University of Ukraine "Kiev Polytechnic Institute," Kyiv, Ukraine, and prepared his Ph.D. in Control Theory applied to Microelectronic System Design at the French Atomic Energy and Alternative Energies Commission (CEA, LETI, Minatec), Grenoble, France.

He is currently an Assistant Professor in auto-

matic control and signal processing at École Centrale de Lyon, Ecully, France. His research interests include design and control of large-scale network systems, multiagents, decentralized control, robust control, and uncertainty impacts.



Gérard Scorletti received the engineering degree from the Ecole Nationale Supérieure de Techniques Avancées, Paris, France, and the Ph.D. degree in automatic control from Orsay University, Orsay, France, in June 1997.

He was a Research Associate with the Centre for Systems Engineering Applied Mechanics, Belgium, from 1997 to 1998. From 1998 to 2007, he was an Assistant Professor of electrical engineering with Caen University, France, and he held a permanent joint appointment with the GREYC. He is currently a Pro-

fessor of automatic control at Ecole Centrale de Lyon, Ecully, France, with a permanent joint appointment with the Ampère Laboratory. His research interests include robust and LPV control, gain scheduling control, and identification for control and linear matrix inequality applications with a special emphasis on CAD.



Christian Vollaire received the M.S. degree in electrical engineering from the University of Saint Jérôme, Marseille, France, in 1992, and the Ph.D. degree in electrical engineering from the Ecole Centrale de Lyon, Lyon, France, in 1997.

From 1997 to 1998, he was a Graduate Research Assistant with the Ecole Centrale de Lyon. In 1998, he joined the AMPERE Laboratory (UMR CNRS 5005), Ecully, France. He carries out its research with AM-PERE (CNRS 5005) at the Ecole Centrale de Lyon in the numerical modeling applied to the interaction

between electromagnetic field and complex systems. He develops in particular specific formulations and numerical methods for the computation of electromagnetic fields in complex structures. One of the fields of applications relates to electromagnetic compatibility.



Florent Morel is an Alumnus of École Normale Supérieure de Cachan, Cachan, France. He received the "agrégation" in electrical engineering and the M.Sc. degree in electrical engineering and the Ph.D. degree in the control of electrical drives from the Institut National des Sciences Appliquées de Lyon, Lyon, France.

Since 2007, he has been an Associate Professor at the Ampère Laboratory, École Centrale de Lyon, Ecully, France. He teaches electrical engineering and power electronics. He considered especially the asso-

ciation of a permanent magnet synchronous machine with a three-phase matrix converter. His research interests include the prediction of conducted emissions from power electronics converters and inductive wireless power transfer in order to optimize these systems.



Laurent Krähenbühl received the Graduate degree in electrical engineering from École Polytechnique Fédérale de Lausanne, Lausanne, Switzerland, in 1978, and the Ph.D. degree in numerical methods for electromagnetic field computation from École Centrale de Lyon, Ecully, France, in 1983.

He worked with Siemens Germany in energy networks. In 1985, he was appointed as a French CNRS Researcher at the Ampère Laboratory, Ecully, and was promoted to a Senior Researcher in 1995. He has supervised more than 20 doctoral students and

has coauthored 80 publications on numerical methods dedicated to the lowfrequency electromagnetism and their application to the optimal design of electrical systems. He has developed strong international links with Belgium and Brazil.